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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.			
10/735,364	12/12/2003	Varadarajan Devnath	P05762	1814			
23418	23418 7590 03/29/2005			EXAMINER			
VEDDER PRICE KAUFMAN & KAMMHOLZ 222 N. LASALLE STREET			MOTTOLA, STEVEN J				
CHICAGO, I	-	ART UNIT	PAPER NUMBER				
			2817				
			DATE MAILED: 03/29/2005	5			

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicati	on No.	Applicant(s)				
Office Action Summary		10/735,3		DEVNATH, VARADAR	RAJAN	(6m)		
		Examine	7	Art Unit				
		Steven J.	Mottola	2817				
Period fo	The MAILING DATE of this commun	nication appears on th	e cover sheet with the c	orrespondence addres	is	-		
A SH THE I - Exter after - If the - If NO - Failu Any	ORTENED STATUTORY PERIOD F MAILING DATE OF THIS COMMUN nsions of time may be available under the provision SIX (6) MONTHS from the mailing date of this com period for reply specified above is less than thirty (period for reply is specified above, the maximum s re to reply within the set or extended period for repl reply received by the Office later than three months ed patent term adjustment. See 37 CFR 1.704(b).	IICATION. s of 37 CFR 1.136(a). In no exmunication. 30) days, a reply within the statatutory period will apply and wy will, by statute, cause the apy	rent, however, may a reply be tin tutory minimum of thirty (30) day rill expire SIX (6) MONTHS from Dication to become ABANDONE	nely filed s will be considered timely. the mailing date of this commu D (35 U.S.C. § 133).	inication.			
Status								
1)□	Responsive to communication(s) fil	ed on						
,	This action is FINAL. 2b) This action is non-final.							
,	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Dispositi	ion of Claims							
4)⊠ 5)⊠ 6)□ 7)□	Claim(s) 1-27 is/are pending in the 4a) Of the above claim(s) is/a Claim(s) 1-27 is/are allowed. Claim(s) is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restrict.	are withdrawn from co						
Applicat	ion Papers							
10)⊠	The specification is objected to by the The drawing(s) filed on <u>09 February</u> Applicant may not request that any objected the properties of the properties of the properties of the properties of the specific	2004 is/are: a) ☐ acception to the drawing(s) g the correction is requi	be held in abeyance. Sered if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1	.121(d).			
Priority (under 35 U.S.C. § 119							
12)[a)	Acknowledgment is made of a claim All b) Some * c) None of: 1. Certified copies of the priority 2. Certified copies of the priority 3. Copies of the certified copies application from the Internation	or documents have been documents have been to find the priority documental Bureau (PCT Ru	en received. en received in Applicat ents have been receive lle 17.2(a)).	ion No ed in this National Sta	ge			
	nt(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary Paper No(s)/Mail D					
3) 🔲 Infor	mation Disclosure Statement(s) (PTO-1449 cer No(s)/Mail Date			Patent Application (PTO-15	2)			

Application/Control Number: 10/735,364

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This application is in condition for allowance except for the following formal matters:

Page 9 of the specification refers to a reference voltage which is the sum of Vref and Voff (last three lines of paragraph 22) with respect to fig. 2, while the figure shows "Vref-Voff". Further, on page 10 of the specification the threshold voltage is referred to as "Vref-Voff" in paragraph 24 and "Vref+Voff" in paragraph 25, both still with respect to fig. 2. It is believed that fig. 2 and paragraph 24 erroneously show a difference where a sum is intended.

Prosecution on the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Chiou et al. and Seetharaman et al. show transimpedance amplifier arrangements designed to remove DC offset but do not compare or differentiate between an output signal and a reference signal that includes a component corresponding to an input DC component as claimed in all independent claims of the present application.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven J. Mottola whose telephone number is 571-272-1766. The examiner can normally be reached on M-Th from 8 to 5.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert J. Pascal, can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Steven J. Mottola Primary Examiner

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